

# SRAM

As of '96.3Q

PRODUCT	DESCRIPTION	PART NO.	PACKAGE OPTION	SPEED(ns)	OPERATING CURRENT (mA,MAX)	STANDBY CURRENT (mA,MAX)		AVAILABILITY					
						TTL	CMOS						
64Kbit	8K × 8	HY6264A	PDIP,SOP	70/85/100	50	2	1	NOW					
		HY6264A-I (ET)											
256Kbit	32K × 8 Slow	HY62256A	PDIP,SOP,TSOP-I	55/70/85/100	50	2	1	NOW					
		HY62256A-I (ET)											
		HY62V256(3.3)(LV)											
		HY62256B	PDIP,SOP,TSOP-I	55/70/85/100	8	1	1						
		HY62256B-I (ET)											
		HY62V256B(3.3V)											
	32K × 8 Fast	HY638256	PDIP,SOJ,TSOP-I	15/20/25	100/90/90	30	2	NOW					
		HY62U256B(3.0V)							PDIP,SOP,TSOP-I	100/120/150	2	0.3	0.015
		HY62U256B-I(ET)											
1Mbit	128K × 8 Slow	HY628100A	PDIP,SOP,TSOP-I	55/70/85	10	2	1	NOW					
		HY628100A-I (ET)											
4Mbit	512K × 8	HY638100	SOJ,TSOP II	15/20/25	150/140/140	40	2	'97.3					
		HY628400							PDIP,SOP,TSOP II	55/70/85	10	2	1

NOTE: We have also low Power(Part No.: "L") and low low power(Part No.: "LL")

# Synchronous SRAM

As of '96.3Q

PRODUCT	DESCRIPTION	PART NO.	PACKAGE OPTION	ACCESS TIME(ns)	OPERATING CURRENT (mA,MAX)	STANDBY CURRENT (mA,MAX)		AVAILABILITY
						TTL	CMOS	
1M	32K × 32 ,3.3V	HY67V32100	TQFP	13/15/20	340	25	5	'97.2Q

# MASK ROM

As of '96.3Q

PRODUCT	DESCRIPTION	PART NO.	PACKAGE OPTION	Speed	OPERATING CURRENT (mA,MAX)	STANDBY CURRENT (mA,MAX)		AVAILABILITY
						TTL	CMOS	
4Mbit	512K × 8	HY234000	PDIP	120	20	1	0.02	NOW
	512K × 8 256K × 16	HY234100	PDIP,SOP	120	20	1	0.02	NOW
16Mbit	2M × 8 1M × 13	HY2316105	PDIP,SOP	120/50	60	1	0.02	'96.4Q